APPLICATION DATA SHEET

Electronic Version v14

Stylesheet Version v14.0

Title of Invention

Variable Capacitor Using MOS Gated Diode with Multiple Segments to Limit DC Current

Application Type: regular, utility

Attorney Docket Number: PS-62A

Request Not To Publish

I/We hereby request that the attached application not be published under 37 U.S.C. 122(b).

I/We hereby certify that the invention disclosed in the attached application has not and will not be the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing.

Correspondence address:

Customer Number: 23933

Continuing Data:

This is a Division of US application number 09/683,709, filed 2002-02-05, now Pending.

Inventor Information:

Inventor 1:

Applicant Authority Type: Inventor

Citizenship:USGiven Name:MinFamily Name:Cao

Residence:

City of Residence: Mountain View

State of Residence: CA
Country of Residence: US

Address-1 of Mailing Address: 101 Laurel Way

Address-2 of Mailing Address:

City of Mailing Address: Mountain View

State of Mailing Address: CA

Postal Code of Mailing Address: 94040

Country of Mailing Address: US

Phone:			
Fax:			
E-mail:			

Attorney Information:

practitioner(s) at Customer Number:

23933



as my attorney(s) or agent(s) to prosecute the application identified above, and to transact all business in the United States Patent and Trademark Office connected therewith.